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JC996 U.S. PTO  
09/996758  
11/30/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Tadashi OSHIMA**

Serial No.: **Not Yet Assigned**

Filed: **November 30, 2001**

For: **SEMICONDUCTOR DEVICE WITH TWO TYPES OF FET's HAVING DIFFERENT GATE LENGTHS AND ITS MANUFACTURE METHOD**

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, D.C. 20231

November 30, 2001

Sir:

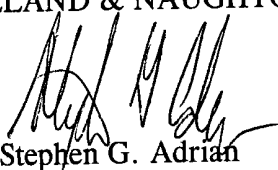
In compliance with 37 CFR 1.56, Applicant calls to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

Copies of the references are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge

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Respectfully submitted,  
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Enclosures: PTO-1449; References (6)